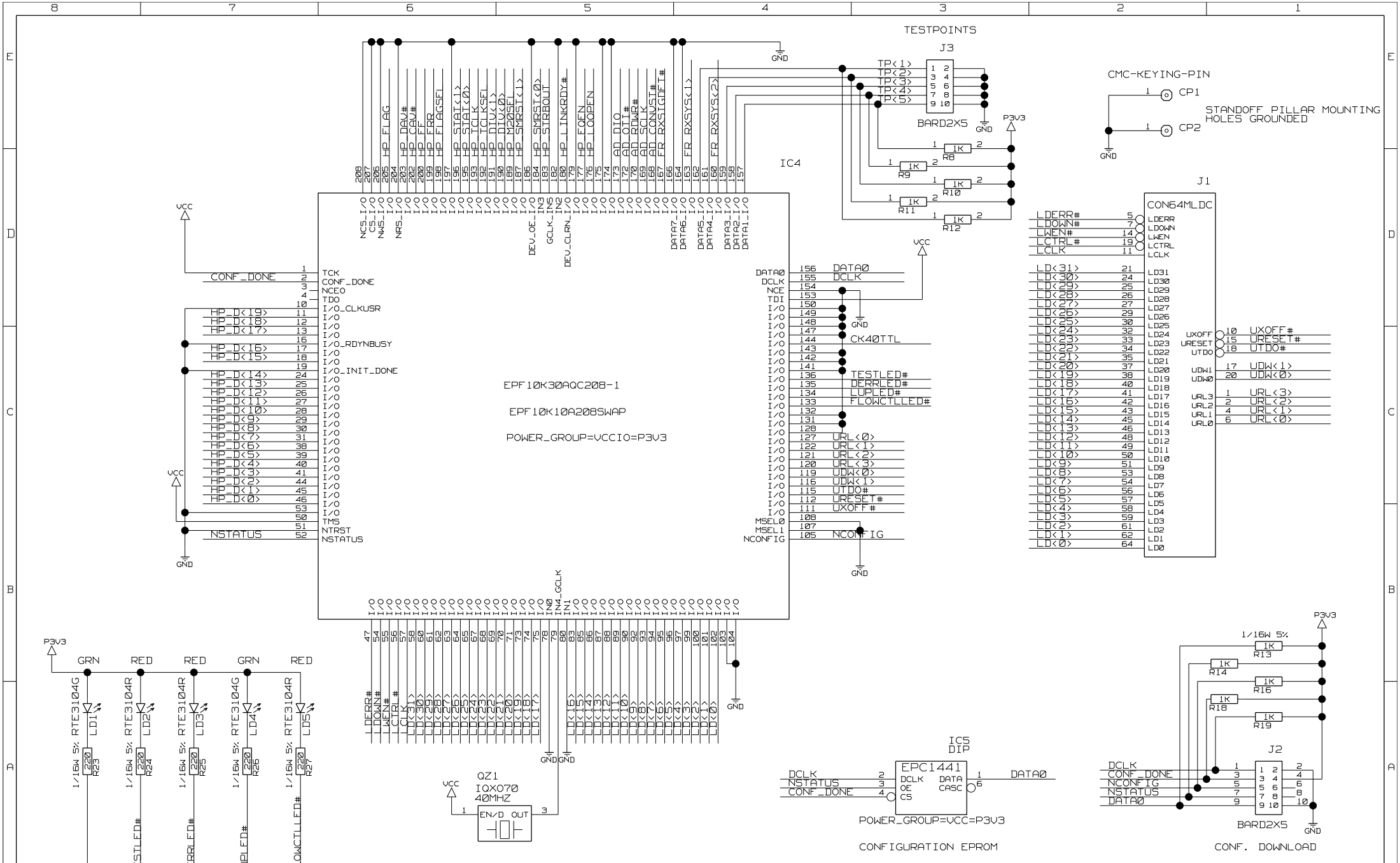

NOTES: MOVE LCLK TO A PIN NEXT TO A GND PIN
DISTRIBUTE I/O CONNECTED TO GND BETWEEN LXX SIGNALS
MOUNT BOTH BARD2X5 AT TOPSIDE OF BOARD

REF: DRAWING G-LDC.LOGIC.1.2	TITLE: G-LDC SIMPLEX G-LINK S-LINK
CERN CN/CE DIV. EP 1211 GENEVA 23 SWITZERLAND	ABBREV: G_LDC — PAGE: 2 OF 2
	LAST_MODIFIED=Tue Sep 15 14:30:16 1998
	DESSIN: VAN DER BIJ — ETUDE: VDB — DATE: 15-9-1998