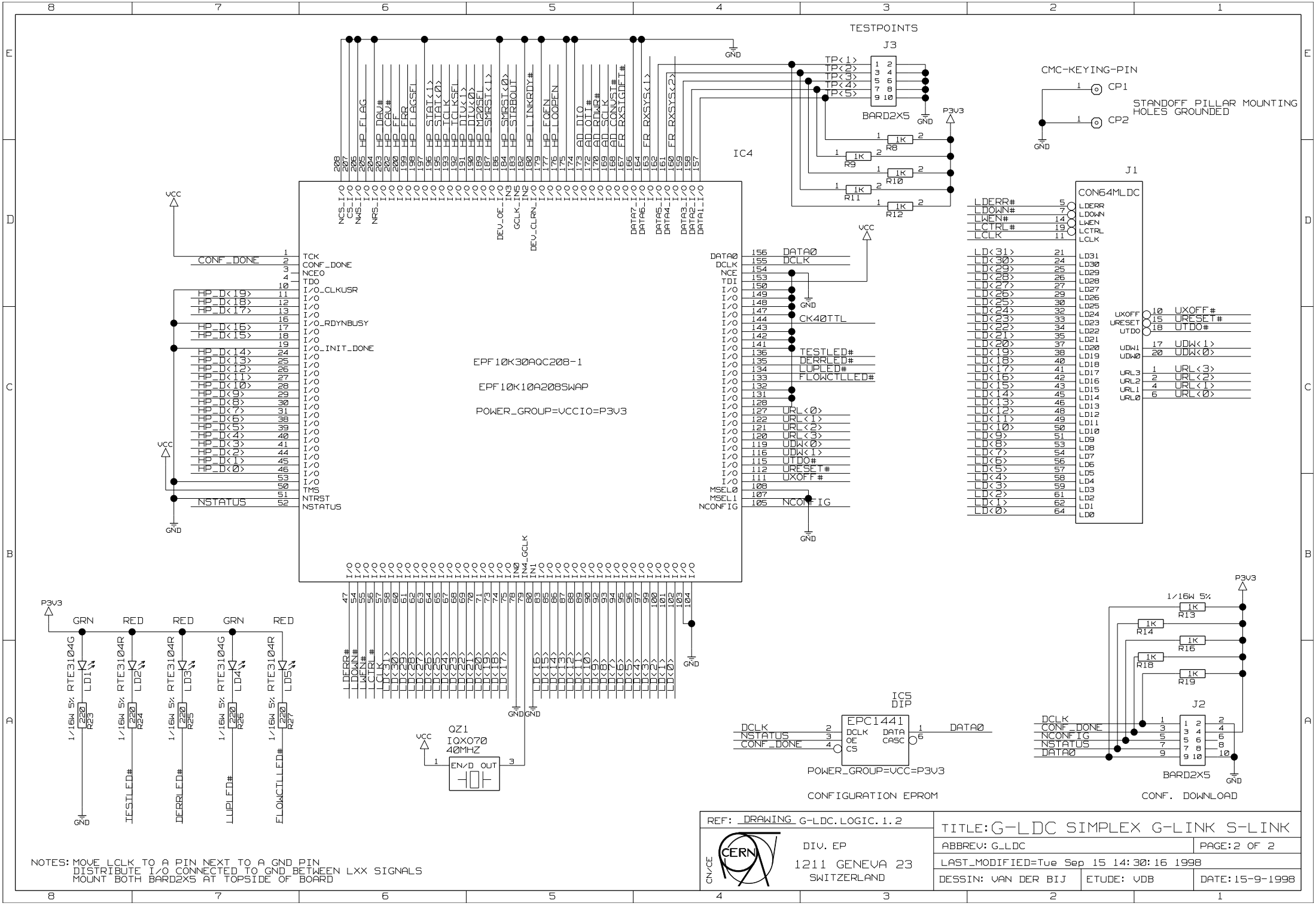
TESTPOINTS
J3
BARD2X5
CMC-KEYING-PIN
CP1
CP2
STANDOFF PILLAR MOUNTING HOLES GROUNDED
IC4
EPF10K30AQC208-1
EPF10K10A208SWAP
POWER_GROUP=VCCIO=P3V3
J1
CON64MLDC
QZ1
IQXO70
40MHZ
IC5
DIP
EPC1441
POWER_GROUP=VCC=P3V3
CONFIGURATION EPROM
J2
CONF. DOWNLOAD
GRN
RED
RTE3104G
RTE3104R
1/16W 5%
220
TESTLED#
DERRLED#
LUPLED#
FLOWCTLLED#
NOTES: MOVE LCLK TO A PIN NEXT TO A GND PIN
DISTRIBUTE I/O CONNECTED TO GND BETWEEN LXX SIGNALS
MOUNT BOTH BARD2X5 AT TOPSIDE OF BOARD
REF: DRAWING G-LDC.LOGIC.1.2
DIV. EP
1211 GENEVA 23
SWITZERLAND
TITLE: G-LDC SIMPLEX G-LINK S-LINK
ABBREV: G_LDC
PAGE: 2 OF 2
LAST_MODIFIED=Tue Sep 15 14:30:16 1998
DESSIN: VAN DER BIJ
ETUDE: VDB
DATE: 15-9-1998