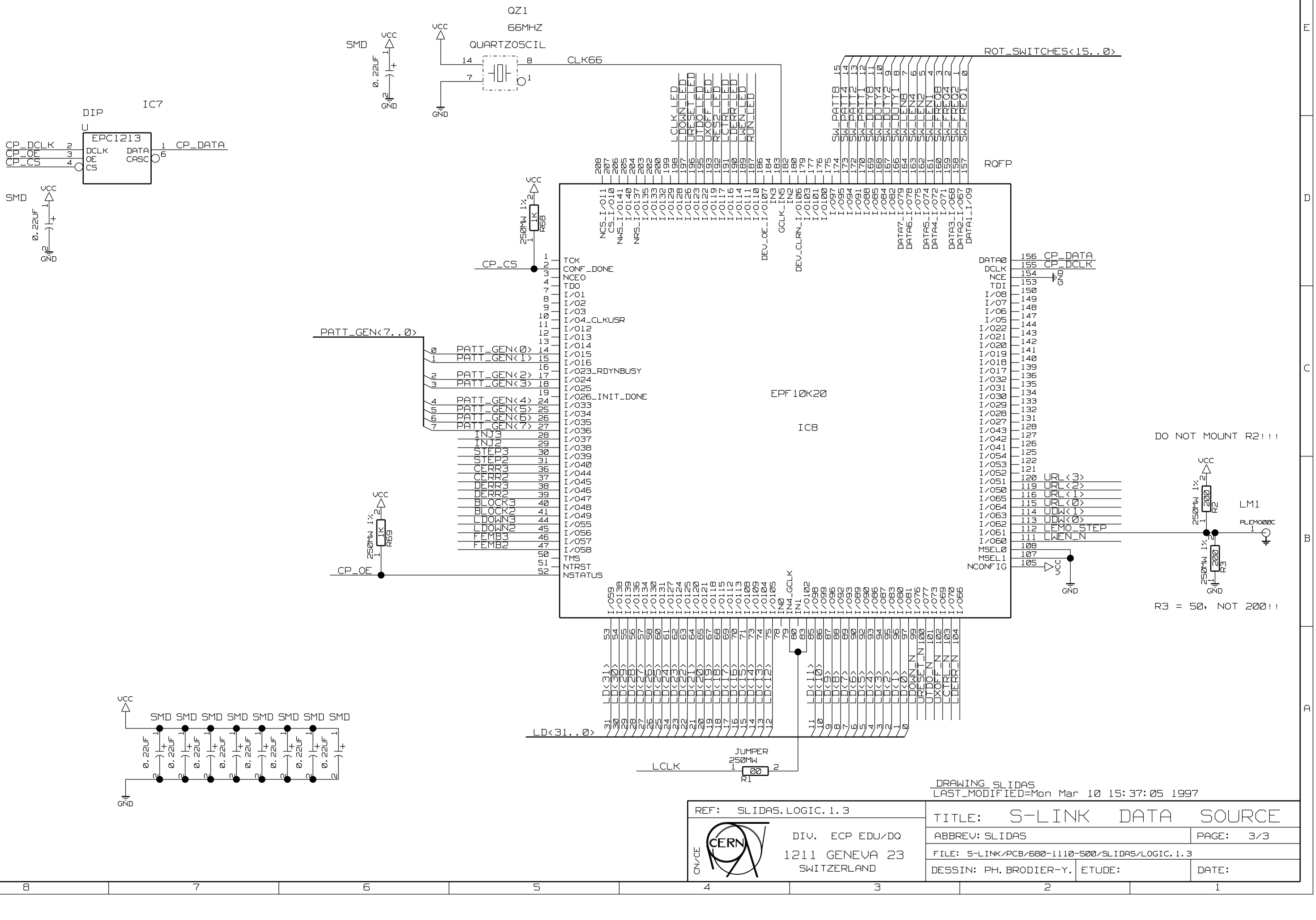
QZ1
66MHZ
QUARTZOSCIL
CLK66
SMD
0.22UF
GND
VCC
IC7
DIP
U
EPC1213
CP_DCLK
CP_OE
CP_CS
DCLK
OE
CS
DATA
CASC
CP_DATA
ROT_SWITCHES<15..0>
SW_PATT8
SW_PATT4
SW_PATT2
SW_PATT1
SW_DUTY8
SW_DUTY4
SW_DUTY2
SW_DUTY1
SW_LEN8
SW_LEN4
SW_LEN2
SW_LEN1
SW_FREQ8
SW_FREQ4
SW_FREQ2
SW_FREQ1
CLK_LED
LDOWN_LED
URESET_LED
UTDO_LED
UXOFF_LED
RES2_LED
LCTRL_LED
LDERR_LED
LXEN_LED
RUN_LED
250MW 1%
1K
R68
R69
RQFP
EPF10K20
IC8
PATT_GEN<7..0>
PATT_GEN<0>
PATT_GEN<1>
PATT_GEN<2>
PATT_GEN<3>
PATT_GEN<4>
PATT_GEN<5>
PATT_GEN<6>
PATT_GEN<7>
INJ3
INJ2
STEP3
STEP2
CERR3
CERR2
DERR3
DERR2
BLOCK3
BLOCK2
LDOWN3
LDOWN2
FEMB3
FEMB2
CP_CS
CP_OE
TCK
CONF_DONE
NCEO
TDO
TMS
NTRST
NSTATUS
I/O4_CLKUSR
I/O23_RDYNBUSY
I/O26_INIT_DONE
DEV_OE_I/O107
GCLK_IN5
DEV_CLRN_I/O106
NCS_I/O11
CS_I/O10
NWS_I/O141
NRS_I/O137
IN3
IN2
IN4_GCLK
IN1
DATA0
DCLK
NCE
TDI
MSEL0
MSEL1
NCONFIG
URL<3>
URL<2>
URL<1>
URL<0>
UDW<1>
UDW<0>
LEMO_STEP
LWEN_N
DO NOT MOUNT R2!!!
LM1
PLEMO00C
R2
R3
R3 = 50, NOT 200!!
LD<31..0>
LD<31>
LD<30>
LD<29>
LD<28>
LD<27>
LD<26>
LD<25>
LD<24>
LD<23>
LD<22>
LD<21>
LD<20>
LD<19>
LD<18>
LD<17>
LD<16>
LD<15>
LD<14>
LD<13>
LD<12>
LD<11>
LD<10>
LD<9>
LD<8>
LD<7>
LD<6>
LD<5>
LD<4>
LD<3>
LD<2>
LD<1>
LD<0>
LDOWN_N
URESET_N
UTDO_N
UXOFF_N
LCTRL_N
LDERR_N
LCLK
JUMPER
250MW
00
R1
SMD SMD SMD SMD SMD SMD SMD SMD
DRAWING SLIDAS
LAST_MODIFIED=Mon Mar 10 15:37:05 1997
REF: SLIDAS.LOGIC.1.3
CERN
CN/CE
DIV. ECP EDU/DQ
1211 GENEVA 23
SWITZERLAND
TITLE: S-LINK DATA SOURCE
ABBREV: SLIDAS
PAGE: 3/3
FILE: S-LINK/PCB/680-1110-500/SLIDAS/LOGIC.1.3
DESSIN: PH.BRODIER-Y.
ETUDE:
DATE: