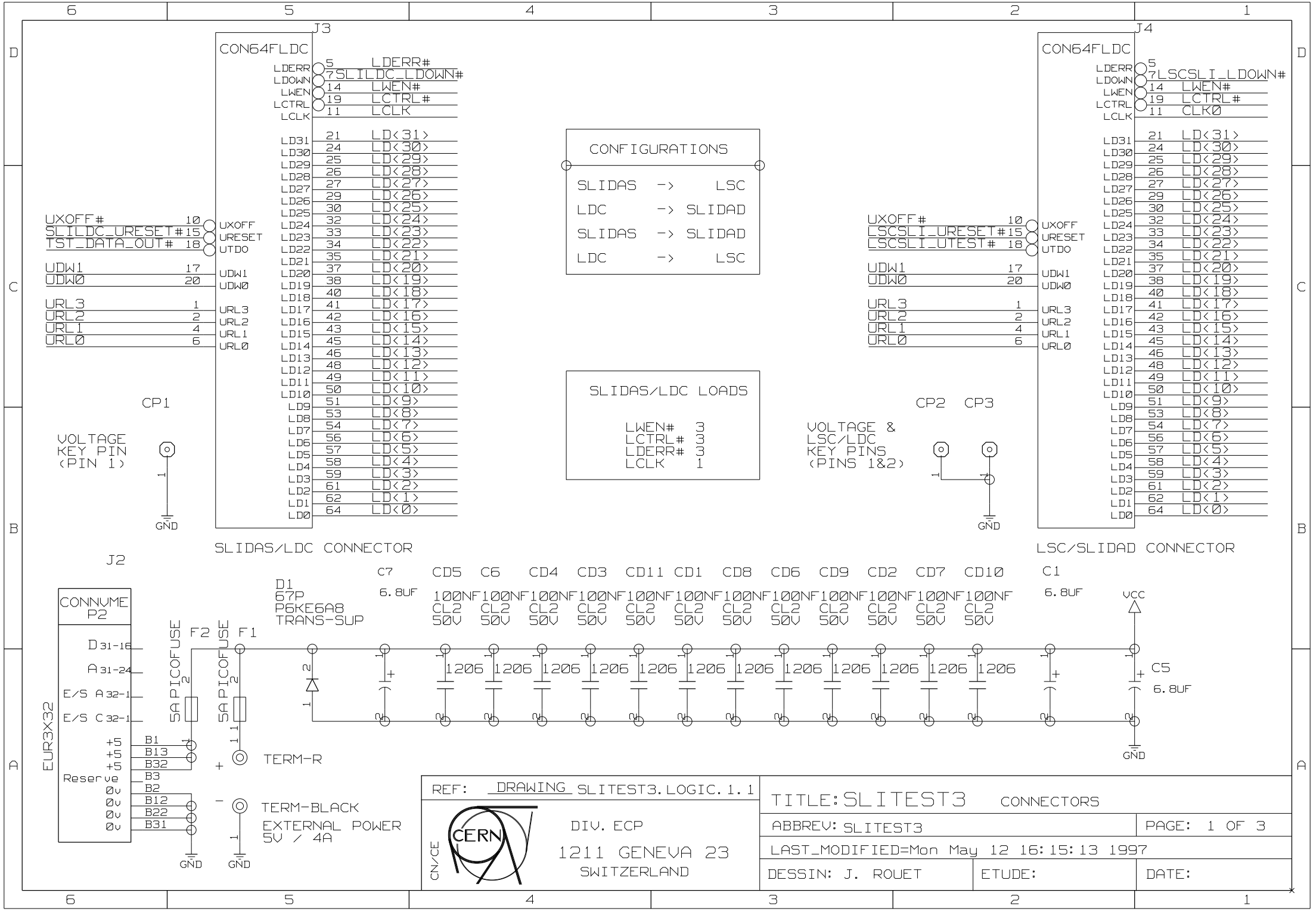
J3
CON64FLDC
LDERR 5 LDERR#
LDOWN 7 SLILDC_LDOWN#
LWEN 14 LWEN#
LCTRL 19 LCTRL#
LCLK 11 LCLK
UXOFF# 10 UXOFF
SLILDC_URESET# 15 URESET
TST_DATA_OUT# 18 UTDO
UDW1 17 UDW1
UDW0 20 UDW0
URL3 1 URL3
URL2 2 URL2
URL1 4 URL1
URL0 6 URL0
LD31 21 LD<31>
LD30 24 LD<30>
LD29 25 LD<29>
LD28 26 LD<28>
LD27 27 LD<27>
LD26 29 LD<26>
LD25 30 LD<25>
LD24 32 LD<24>
LD23 33 LD<23>
LD22 34 LD<22>
LD21 35 LD<21>
LD20 37 LD<20>
LD19 38 LD<19>
LD18 40 LD<18>
LD17 41 LD<17>
LD16 42 LD<16>
LD15 43 LD<15>
LD14 45 LD<14>
LD13 46 LD<13>
LD12 48 LD<12>
LD11 49 LD<11>
LD10 50 LD<10>
LD9 51 LD<9>
LD8 53 LD<8>
LD7 54 LD<7>
LD6 56 LD<6>
LD5 57 LD<5>
LD4 58 LD<4>
LD3 59 LD<3>
LD2 61 LD<2>
LD1 62 LD<1>
LD0 64 LD<0>
SLIDAS/LDC CONNECTOR
CONFIGURATIONS
SLIDAS -> LSC
LDC -> SLIDAD
SLIDAS -> SLIDAD
LDC -> LSC
SLIDAS/LDC LOADS
LWEN# 3
LCTRL# 3
LDERR# 3
LCLK 1
CP1
VOLTAGE KEY PIN (PIN 1)
GND
CP2 CP3
VOLTAGE & LSC/LDC KEY PINS (PINS 1&2)
J4
CON64FLDC
LDERR 5
LDOWN 7 LSCSLI_LDOWN#
LWEN 14 LWEN#
LCTRL 19 LCTRL#
LCLK 11 CLK0
UXOFF# 10 UXOFF
LSCSLI_URESET# 15 URESET
LSCSLI_UTEST# 18 UTDO
UDW1 17 UDW1
UDW0 20 UDW0
URL3 1 URL3
URL2 2 URL2
URL1 4 URL1
URL0 6 URL0
LSC/SLIDAD CONNECTOR
J2
CONNVME P2
D31-16
A31-24
E/S A32-1
E/S C32-1
+5 B1
+5 B13
+5 B32
Reserve B3
0v B2
0v B12
0v B22
0v B31
EUR3X32
5A PICOFUSE F2
5A PICOFUSE F1
TERM-R
TERM-BLACK
EXTERNAL POWER 5V / 4A
D1 67P P6KE6A8 TRANS-SUP
C7 6.8UF
CD5 CD6 CD4 CD3 CD11 CD1 CD8 CD6 CD9 CD2 CD7 CD10
100NF CL2 50V
1206
C1 6.8UF
C5 6.8UF
VCC
REF: DRAWING SLITEST3.LOGIC.1.1
DIV. ECP
1211 GENEVA 23
SWITZERLAND
CN/CE
TITLE: SLITEST3 CONNECTORS
ABBREV: SLITEST3
PAGE: 1 OF 3
LAST_MODIFIED=Mon May 12 16:15:13 1997
DESSIN: J. ROUET
ETUDE:
DATE: